

SMS0620 Series Ultra-high current SMD power inductors





◆特征:

- 低直流电阻和超大电流的薄型设计
- 磁屏蔽型抗电磁干扰强适用于高密度安装
- 高可靠性,通过采用一体成型结构享有卓越 的抗震动性
- 由于复合结构,超低蜂鸣噪声
- 低损耗合金粉末压铸低阻抗, 小寄生电容
- 能效高,可减少绕线的低直流电阻与磁芯的 涡流损耗
- 频率高达 3MHz
- 绝缘最大电压 30VDC
- 符合 RoHS, 无卤和 REACH

◆用途:

- PDA,笔记本,台式机,服务器应用程序
- 大电流 POL 转换器
- 电池供电设备
- 分布式电源系统中的 DC/DC 转换器

◆环境:

工作温度: -55℃ 至+125℃
 (包括线圈自身温升)

◆试验设备:

- 电感值: WK3260B 或同等仪器
- 电流: WK3260B+WK3265B 或同等仪器
- 直流电阻: Chroma 16502 或同等仪器

◆产品型号:

SMS

Features:

- Low RDC and ultra-high current thin design
- Magnetic shielding type, strong anti- electromagnetic Interference, suitable for high- density installation
- High-reliability, High vibration resistance as result of newly developed integral construction
- Ultra Low buzz noise, due to composite construction
- Die-casting by low loss alloy powder low impedance,
 Small parasitic capacitance
- High efficiency Low DC resistance of winding and low eddy-current loss of the core
- Frequency up to 3MHz
- Absolute maximum voltage 30VDC
- RoHS, Halogen Free and REACH Compliance

Applications:

- PDA , notebook ,desktop ,server applications
- High current POL converters
- Battery powered devices
- DC/DC converters in distributed power systems

Environmental Data:

Operating Temperature: -55°C to +125°C
 (Including coils self-temperature rise)

Test Equipment:

• L: WK3260B LCR meter or equivalent

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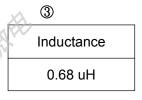
- Isat & Irms: WK3260B+WK3265Bor equivalent
- DCR:Chroma 16502 or equivalent

Product Identification:

	2					
	外形尺寸(L×W×H) (mm) External Dimensions (L×W×H)					
. <						
	(mm)					
	0620	7.1×6.6×2.0				

R68

(3)



0620





(5)

4

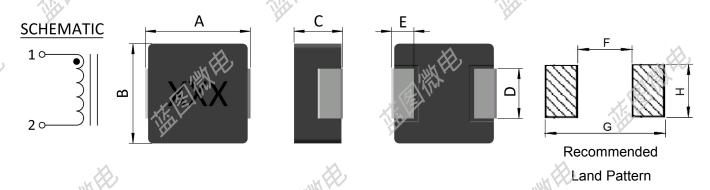
公差 Inductance Tolerance

J:±5%,K: ±10%, L: ±15% M: ±20%,P: ±25%, N: ±30%

•						
	包装 Packing					
В	散装Bulk Package					
Т	编带Tape & Reel					

◆外观尺寸:

Shape and Dimensions (dimensions are in mm):



Part No				ITEM				
y i dit ito	А	В	С	D	É	F	G	Н
SMS0620	7.10±0.30	6.60±0.20	2.00Max	3.00 Typ	1.60 Typ	3.70	8.40	3.50

• SMS0620 Series Electrical Characteristics (Electrical specifications at 25℃)

Down NA	Inductance 100KHz 1.0V		DCF	R (mΩ)	Saturation Current	Heat Rating Current	
Part No	L(μH) '@0A	Tol	Typical	Max	(A) Typical	(A) Typical	
SMS0620-R68M	0.68	±20%	9.50	12.0	16.00	10.00	
SMS0620-1R0M	1.0	±20%	15.0	17.0	14.00	7.60	
SMS0620-1R5M	1.5	±20%	23.0	30.0	12.00	6.50	
SMS0620-2R2M	2.2	±20%	29.0	35.0	10.00	6.00	
SMS0620-3R3M	3.3	±20%	47.0	57.0	7.50	4.50	
SMS0620-4R7M	4.7	±20%	58.0	70.0	6.00	3.50	
SMS0620-6R8M	6.8	±20%	99.0	110.0	4.00	2.90	
SMS0620-100M	10	±20%	130.0	154.0	3.50	2.30	

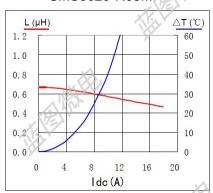
- Saturation Current: DC current at which inductance drops 30% from its value without current.
- Heat Rating Current: the actual value of DC current when the temperature rise is ΔT 40 °C (Ta=25 °C).
- Rated DC Current: The less value which is Isat or Irms.
- Special remind:Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.



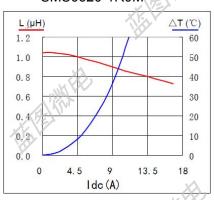


• Saturation current VS temperature rise current curve

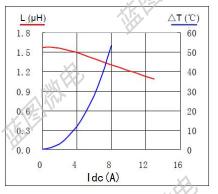
SMS0620-R68M



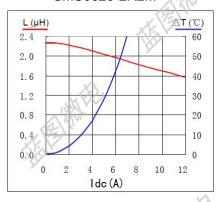
SMS0620-1R0M



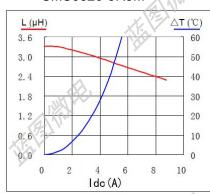
SMS0620-1R5M



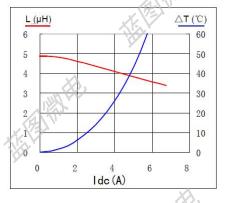
SMS0620-2R2M



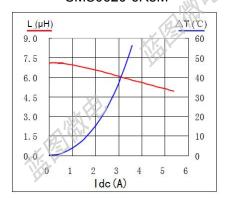
SMS0620-3R3M



SMS0620-4R7M

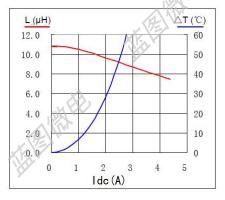


SMS0620-6R8M



SHENZHEN LANTU MICRO ELE

SMS0620-100M

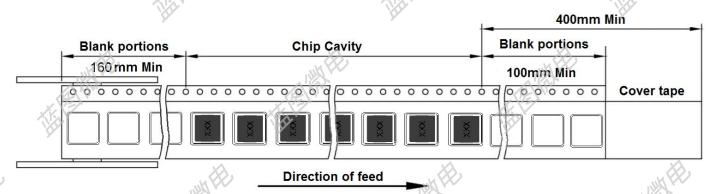




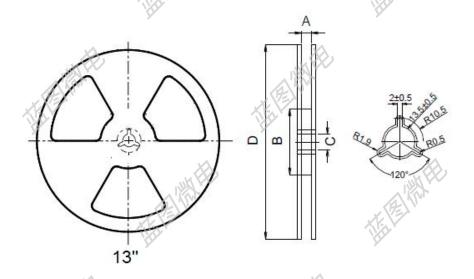


◆产品包装: Packaging:

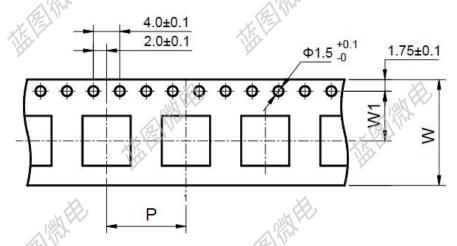
• Tape and Reel Specifications: (Dimensions are in mm)



• Reel dimensions (mm)



●Tape Dimension (mm)

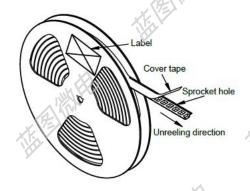


Part No.	Tape Dimension		Reel Dimensions			REEL	Inside	Outside		
Paterio.	W	Р	W1	Α	В	С	D	(PCS)	Box(PCS)	Carton(PCS)
SMS0620	16.0	12.0	7.5	16.4	100	13	330	2000	6000	24,000

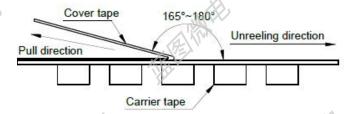




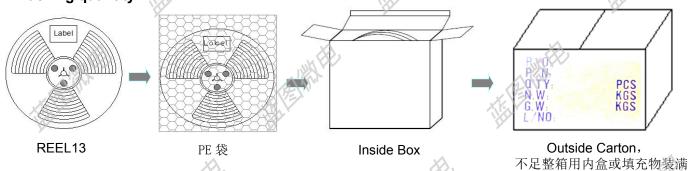
• Cover tape peel off condition



- a) Cover tape peel force shall be 10 to 120g
- b) Noodle strip peeling angle165° to 180°



• Packing quantity







◆可靠性测试:

Reliability Testing:

	Menability resultg:
Requirements	Test Methods and Remarks
1. Pulling test: Define: A: sectional area of terminal A ≤ 8mm2 force ≥ 5N time:30sec 8mm2 <a 10n="" 10sec="" 2.="" 20mm2="" 20mm2<a="" 20n="" 3.="" above="" any="" force="" loose="" meet="" paste="" requirements="" solder="" td="" terminal<="" the="" thickness:0.12mm="" time:="" without="" ≤="" ≥=""><td>Solder the inductor to the testing jig using leadfree solder. Then apply a force in the Keep time: 10±1s Speed: 1.0mm/s.</td>	Solder the inductor to the testing jig using leadfree solder. Then apply a force in the Keep time: 10±1s Speed: 1.0mm/s.
1.Terminal diameter(d) mm 0.35 < d ≤ 0.50Applied force:5N Duration: 10sec2.Terminal diameter(d) mm0.50 < d ≤ 0.80Applied force:10N Duration: 10sec3.Terminal diameter(d) mm0.80 < d ≤ 1.25Applied force:20N Duration: 10sec4.Terminal diameter(d) mmD > 1.25Applied force:40N Duration: 10sec5.Meet the above requirements without any loose terminal.	Pull Force:the force shall be applied gradually to the terminal and thenmaintained for 10 seconds. F Pulling test
1.No visible mechanical damage.	1.Solder the inductor to the test jig (glass epoxy board 2.shown in Using a leadfree solder. Then apply a force in the direction shown 3.Flexure: 2mm. 4.Pressurizing Speed: 0.5mm/sec. 5.Keep time: 30 sec.
1.No case deformation or change	1.Drop the packaged products from 1m high in 1
inappearance.	angle, 3 ridges and 6surfaces, twice in each
2.No short and no open.	direction.
3.Terminals must have 95% minimum solder	1.Solder temperture:240±2℃ 2.Duration: 3 sec 3. Solder: Sn/3.0Ag/0.5Cu. 4.Flux: 25% Resin and 75% ethanol in weight
	1. Pulling test: Define: A: sectional area of terminal A ≤ 8mm2 force ≥ 5N time:30sec 8mm2 <a 0.35="" 0.50applied="" 0.80applied="" 1.="" 1.25applied="" 10n="" 10sec="" 10sec2.="" 10sec3.="" 10sec4.="" 2.="" 20mm2="" 20mm2<a="" 20n="" 3.="" <="" above="" any="" d="" diameter(d)="" duration:="" force="" force:10n="" force:20n="" force:5n="" loose="" meet="" mm="" mm0.50="" mm0.80="" mmd="" paste="" requirements="" solder="" terminal="" the="" thickness:0.12mm="" time:="" without="" ≤="" ≥=""> 1.25Applied force:40N Duration: 10sec5. Meet the above requirements without any loose terminal. 1. No visible mechanical damage. 1. No visible mechanical damage. 2. Wetting shall exceed 75% coverage for 3. Terminals must have 95% minimum solder





Items	Requirements	Test Methods and Remarks
	1 No visible mechanical damage.	1.Solder the inductor to the testing jig (glass epoxy
	2. Inductance change: Within ±10%.	boardshown in) using leadfree solder.
	3.Q factor change: Within ±20%.	2.The inductor shall be subjected to a simple
with	Cu pad Solder mask	harmonic motion having total amplitude of 1.5mm,
		the frequency being varieduniformly between the
75		approximate limits of 10 and 55 Hz.
Vibration		3.The frequency range from 10 to 55 Hz and
Reference documents:		return to 10 Hz shallbe traversed in approximately
GB/T 2423.10-2019	Glass Epoxy Board	1 minute. This motion shall be applied for a period
振動試验	E TO	of 2 hours in each 3mutually perpendicular
	THE P	directions(total of 6 hours).
	Y	Freq
"性	A thus	55Hz
E VIV	A TOP	10Hz
A LANGE	THE LESS OF THE PARTY OF THE PA	0 1Min Time
7	1.No visible mechanical damage.	1.Start at (85~125℃) for T time, rush to
	2. Inductance change: Within ±10%.(Mn-Zn:	(-55~40℃) for T time as one cycle, go through100
	Within ≤ 30%)	cycles.
Thermal Shock	3.Q factor change: Within ±20%.	2.Transforming interval: Max. 20 sec.
	The last the	3.Tested cycle: 100 cycles.
Reference documents:		4.The chip shall be stabilized at normal condition
GB/T 2423.22-2012 Method Na		for 1~2 hours
冷热冲击试验		125°C/85°C 30 min. 30 min.
44 3844 11 11 12 13	XX. PELL	Ambient
	With the second	Temperature 30 min.
		20sec. (max.)
	A Human	IN FEE
	1.No visible mechanical damage.	1.Temperature:M(-55~-40±2℃)
	2. Inductance change: Within ±10%.(Mn-Zn:	2.Duration: 96±2 hours
Low temperature Storage	Within ≦30%)	3.The chip shall be stabilized at normal condition for
Reference documents:	3.Q factor change: Within ±20%.	1~2 hoursbefore measuring.
GB/T 2423.1-2008	1994	Room
Method Ab	N. Baylow	Temp 96H Test
低温储存试验	THE P	97H 98H Time
y	<i>"</i>	M°C Low temperature
	<i>X</i> .	Temp





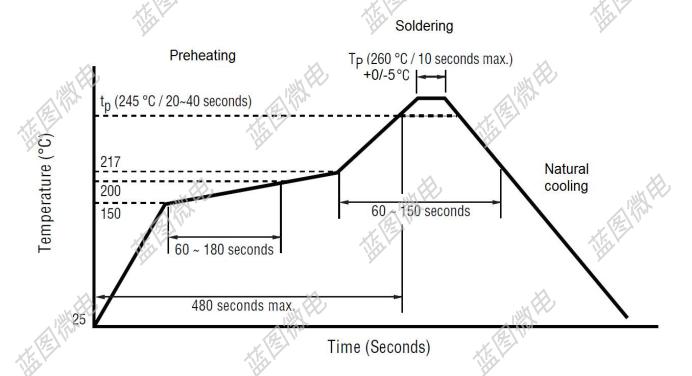
14	-	THE RESERVE TO THE STATE OF THE
Items	Requirements	Test Methods and Remarks
High temperature	1. No visible mechanical damage.2. Inductance change: Within ±10%.(Mn-Zn:	1.Temperature:N(125~85 \pm 2 $^{\circ}$). 2.Duration: 96 \pm 2 hours
Storage	Within ≦30%)	3.The chip shall be stabilized at normal condition
Reference documents: GB/T 2423.2-2008 Method Bb 高温储存试验	3.Q factor change: Within ±20%.	for 1~2 hoursbefore measuring. Temp High temperature Room Temp 0 96H 97H 98H Time
Damp Heat	 1.No visible mechanical damage. 2. Inductance change: Within ±10%.(Mn-Zn: Within ≤30%) 	1.Temperature: 60±2°C 2.Humidity: 90% to 95% RH. 3.Duration: 96±2 hours.
l .	Y	Y
(Steady States) Reference documents: GB/T 2423.3-2016 恒定湿热试验	3.Q factor change: Within ±20%.	4. The chip shall be stabilized at normal condition for 1~2 hoursbefore measuring. Temp 60°C 93%RH Room Conditions 0 96H 97H 98H Time
Heat endurance of Reflow soldering Reference documents: GJB 360B-2009 回流焊耐热性试验	1.No significant defects in appearance. 2. △ L/L ≦ 10% (Mn-Zn: △ L/L ≦ 30%) 3. △ Q/Q ≦ 30% (SMD series only) 4. △ DCR/DCR ≦ 10%	1.Refer to the above reflow curve and go through the reflow for twice. 2.The peak temperature : 260+0/-5°C
Resistance to solvent test Reference documents: IEC 68-2-45:1993 耐溶剂性试验	No case deformation or change in appearance or obliteration of marking	To dip parts into IPA solvent for 5±0.5Min,then drying them at room temp for 5Min,at last ,to brushing making 10 times.
Overload test Reference documents: JIS C5311-6.13 过负荷试验	1.During the test no smoke, no peculiar, smell, no fire 2.The characteristic is normal after test	Apply twice as rated current for 5 minutes.
voltage resistance test Reference documents: MIL-STD-202G Method 301 绝缘耐压测试	During the test no breakdown The characteristic is normal after test	For parts with two coils DC1000V, Current: 1mA, Time: 1Min. Refer to catalogue of specific products
		L





◆推荐回流焊温度曲线

Recommended reflow soldering curve:



The recommended reflow conditions as above graph, is set according to our soldering equipment. DUE to various manufactures may have different reflow soldering equipment, products, process conditions, set methods. And so on, when setting the reflow conditions, Please adjust and confirm according to users' environment/equipment.





使用注意事项 REMINDERS FOR USING THESE PRODUCTS



● 保存时间为12 个月以内,保存条件(温度5~40°C以下、湿度35 ~ 66%RH 以下),需充分注意。 若超过保存时间,端子电极的可焊性将可能老化。

The storage period is within 12 months. Be sure to follow the storage conditions (temperature: 5~40°C, humidity: 35 to 65% RH or less). If the storage period elapses, the soldering of the terminal electrodes may deteriorate.

- 请勿在气体腐蚀环境(盐、酸、碱等)下使用和保存。
 Do not use or store in locations where there are conditions such as gas corrosion (salt, acid, alkali, etc.).
- 手上的油脂会导致可焊性降低,应避免用手直接接触端子。
 Don't touch electrodes directly with bare hands as oil secretions may inhibit soldering Always ensure optimum conditions for soldering。
- 请小心轻拿轻放,避免由于产品的跌落或取出不当而导致的损坏。
 Please always handle products carefully to prevent any damage caused bydropping down or inappropriate removing。
- 端子过度弯曲会导致断线,请不要过度弯曲端子。
 Don't bend the terminals with excessive stress in case of any wire fracture.
- 不要清洗产品,如需要清洗时请联系我司。
 Don't rinse coils by yourself and please contact SXN if necessary。
- 请勿将本产品靠近磁铁或带有磁力的物体
 Don't expose the products to magnets or magnetic fields
- 在实施焊接前,请务必进行预热。预热温度与焊接温度及芯片温度的温度差要在150°C 以内。 Before soldering, be sure to preheat components. The preheating temperature should be set so that the temperature difference between the solder temperature and chip temperature does not exceed 150°C.
- 安装后的焊接修正应在规格书规定的条件范围内。若加热过度可能导致短路、性能降低、寿命减少。
 Soldering corrections after mounting should be within the range of the conditions determined in the specifications. If overheated, a short circuit, performance deterioration, or lifespan shortening may occur.
- 装置会因通电而自我发热(温度上升),因此在热设计方面需留有充分余地。
 Self heating (temperature increase) occurs when the power is turned ON, so the tolerance should be sufficient for the set thermal design.
- 非磁屏蔽型在基板设计时需注意配置线圈,受到电磁干扰可能会导致误动作。
 Carefully lay out the coil for the circuit board design of the non-magnetic shield type. A malfunction may occur due to magnetic interference.